



NetLogic Adds Speedy Interface

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MOUNTAIN VIEW, Calif. — NetLogic Microsystems, Inc. (NASDAQ:NETL - News), a worldwide leader in high-performance intelligent semiconductor solutions for next-generation Internet networks, today announced the NL111024 processor, the world's first knowledge-based processor with high-speed serial interface, and a member of its sixth-generation knowledge-based processor family. The integration of high-speed serial interface delivers 225Gbps of raw chip-to-chip interconnect bandwidth. This represents a 340% increase in I/O bandwidth-per-pin to enable significantly higher system performance, higher system density and lower system costs for next-generation systems to enable significantly richer services for LTE, IPTV and IPv6 services. In addition, the NL111024 processor features an enhanced knowledge-based processing core capable of achieving 1.6 billion decisions per second (BDPS).

The exponential growth in network services and applications for next-generation LTE and IPTV deployments are driving the need for increased classification and security processing per packet. This is in turn driving the need for higher performance knowledge-based processors with high-speed serial links to significantly enhance the interconnect bandwidth and communications between the knowledge-based processors and system packet processors such as multi-core processors, network processors and customer ASICs.

In addition, the migration from IPv4 to IPv6 is resulting in 4x wider addresses for Layers 2-4 processing. This translates into the need for higher I/O bandwidth and larger knowledge database density and capacity. The higher I/O bandwidth of the NL111024 processor delivers an improved balance between the knowledge-based processing core and the I/O performance to allow wider IPv6 keys to be processed at the same search rate as narrower IPv4 keys.

In addition to the higher I/O bandwidth, the narrow-bus differential signaling of the NL111024 processor's high-speed serial interface also enables customers to develop system boards with a lower layer count, thereby reducing the complexity and costs of next-generation systems.

“Our NL111024 knowledge-based processor is truly revolutionary as it is designed and optimized to meet and exceed the needs of our customers who are looking to simultaneously support much richer services and IPv6 capabilities,” said Chris O’Reilly, vice president of marketing at NetLogic Microsystems. “By raising the bar on the knowledge-based processing performance to 1.6BDPS and enhancing the I/O bandwidth by 340%, this NL111024 device showcases the true R&D cross-development synergies within the company as it brings

together our industry-leading knowledge-based processing technology with our best-in-class PHY and SerDes technology.”

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