



NetLogic Intros 40G Chip

JULY 7, 2010

SANTA CLARA, Calif. — NetLogic Microsystems, Inc. [NASDAQ: NETL], a worldwide leader in high-performance intelligent semiconductor solutions for next-generation Internet networks, today announced the industry's first fully deterministic intelligent networking solution capable of concurrently processing advanced Layers 2 - 7 functions at 40Gbps wire-speed. By using market-leading, best-in-class multi-core processing and knowledge-based processing technologies from NetLogic Microsystems, the new innovative NLX1281103A solution enables the simultaneous and deterministic processing of complex networking functions such as deep packet content inspection, application-aware switching and routing, intrusion prevention (IPS), anti-virus/malware, network service management, packet ordering, parsing, IPv6/IPv4 forwarding, classification, IPSec/SSL encryption, ACL security, compression/decompression and quality-of-service (QoS) at 40Gbps wire-speed.

The exponential growth of converged voice, video and data traffic over IP-based networks, coupled with the rapid adoption of services and applications over mobile devices, is driving the need for networking equipment to concurrently support all Layers 2 through 7 functions and to be able to process network traffic at wire speeds and with full determinism. Because available competitive solutions provide non-deterministic, high latency and best-effort packet processing, networking original equipment manufacturers (OEMs) have been forced to compromise network integrity by separating Layers 2 - 4 and Layers 4 - 7 processing on different cards and then selectively performing certain functions only on a fraction of the traffic. NetLogic Microsystems' 40Gbps NLX1281103A solution enables next-generation switches, routers, access aggregation, metro Ethernet, service gateways, security appliances and storage appliances to perform uncompromised L2 - L7 packet processing on every packet of traffic with minimal network latency under a wide variety of Internet traffic scenarios, which in turn ensures predictable network behaviors and eliminates network bottlenecks.

NetLogic Microsystems' 40Gbps wire-speed L2 - L7 solution incorporates 32 NXCPUs™ from its best-in-class XLPTM multi-core, multi-threaded processor family, along with 128 knowledge-based processing engines, 196 Intelligent Fabric for Automata (IFA) engines from its market-leading NETL7™ processor family and over 40 fully-autonomous programmable processing engines - all tightly coupled and working in concert to deliver deterministic wire-speed performance.

"Our new L2 - L7 NLX1281103A solution is a game changer for the industry as it provides customers with the unprecedented ability to develop a new class of highly differentiated networking systems that deliver deterministic line-rate throughput while supporting L2 - L7 functionality," said Chris O'Reilly, vice president of marketing at NetLogic Microsystems. "This

further demonstrates our unique capabilities to drive innovations across multiple product lines and to deliver new levels of performance that are unparalleled in the industry.”

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